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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Masaru WASAKI et al.

Application No.: 10/524,661

Docket No.: 122761

Filed: February 11, 2005

For: COMMON MODE SIGNAL SUPPRESSING CIRCUIT AND NORMAL MODE
SIGNAL SUPPRESSING CIRCUIT

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

James A. Oliff
Registration No. 27,075

Jesse O. Collier
Registration No. 53,839

JAO:JOC/nxy

Date: September 21, 2005

OLIFF & BERRIDGE, PLC
P.O. Box 19928
Alexandria, Virginia 22320
Telephone: (703) 836-6400

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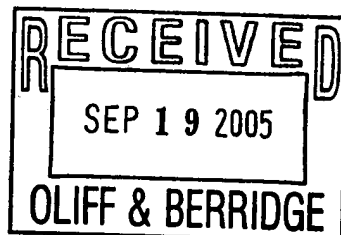


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/524,661	02/11/2005	2817	900	122761	9	14	2

25944
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CONFIRMATION NO. 1511
 FILING RECEIPT
 OC000000016948040
 OC000000016948040

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Applicant(s)

Masaru Wasaki, ~~Chiba~~, JAPAN, Deceased; Ichihara-shi
 Hitomi Wasaki, ~~Chiba~~, JAPAN, Legal Representative; Ichihara-shi
 Yoshihiro Saitoh, Tokyo, JAPAN;

Assignment For Published Patent Application

TDK CORPORATION, TOKYO, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 25944.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP02/40200 08/11/2003 PCT/JP03/10200 08/11/2003

Foreign Applications

JP 2002-238514 (8/19/2002)
 JP 2002-238515 (8/19/2002)

Projected Publication Date: 12/15/2005

Non-Publication Request: No

Early Publication Request: No

Title

Common mode signal suppressing circuit and normal mode signal suppressing circuit

Preliminary Class

333

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